

Innovate

2009 FPGA Competition

Show-off your innovation and design skill

Field Programmable Gate Arrays are one of the quickest and most flexible ways to implement a wide range of applications and systems on chip designs. Work in VHDL, Verilog, or C and show the world your stuff!

Top teams will get their applications published at FCCM'10 and win a modest cash prize.

Innovate North-America is open to undergraduate and graduate students enrolled in a Canadian or American college or university.

Teams are required to have a mentor from their educational institution, or from industry. FPGA design is desirable but not required.

<http://university.altera.com/innovate/na>

Contact: innovate-na@altera.com

Proposal submission deadline:
October 30th 2009

Semi-finalist selecton:
January 2010

Finalist selection:
March 2010

Cash prizes, guaranteed
publication: **FCCM'10**

Organizers

- IEEE Ottawa Section
- IEEE Ottawa chapter of the Technology Management Council
- IEEE Ottawa Section Educational Activities

Sponsors

